

# BEYER WEAVER & THOMAS, LLP

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## FACSIMILE COVER SHEET

March 14, 2005

**Receiver:** Examiner David A . Zarneke  
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DESMUND GEAN

Our Ref. No.: NSC1P236C1/P05221  
Appln. No.: 10/714,028

**Re:** Terminal Disclaimer

Pages Including Cover Sheet(s): 3

**MESSAGE:**

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Mostafazadeh et al.

Attorney Docket No.:  
NSC1P236C1/P05221

Application No.: 10/714,028

Examiner: Zarneke, David A.

Filed: November 14, 2003

Group: 2829

Title: BUMPED INTEGRATED CIRCUITS FOR  
OPTICAL APPLICATIONS**CERTIFICATE OF TRANSMISSION**

I hereby certify that this correspondence is being transmitted via facsimile to the U.S. Patent and Trademark Office, Attention:  
Examiner David A. Zarneke at facsimile telephone number (571) 273-1937 on March 14, 2005.

Printed Name: Tomika Thomas Signed: Tomika Thomas**TERMINAL DISCLAIMER UNDER 37 C.F.R. § 1.321**  
**TO OBLIGATE AN OBVIOUSNESS-TYPE**  
**DOUBLE PATENTING REJECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

National Semiconductor Corporation owns the entire interest in the above-identified application and in the prior United States Patent No. 6,707,148 (hereafter "prior patent"). National Semiconductor Corporation hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. §§154 to 156 and §173 of the prior patent. National Semiconductor Corporation hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the patent and the prior patent are commonly owned. This agreement runs with any patent granted on the above-identified application and is binding upon the grantee, its successors or assigns.

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The undersigned is an agent of record.

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Respectfully submitted,  
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